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Date: **June 24, 2005**

File Number: **5649-1188**

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To: **Mail Stop Amendment
Examiner: Tran, Long K.
Group Art Unit: 2818**

Company: **United States Patent and Trademark Office**

From: **Elizabeth A. Stanek**

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Attorney's Docket No. 5649-1188

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Chung *et al.*

Confirmation No.: 5341

Serial No.: 10/803,640

Group Art Unit: 2818

Filed: March 18, 2004

Examiner: Tran, Long K.

For: **INTEGRATED CIRCUIT DEVICES HAVING BARRIER LAYERS
BETWEEN UPPER ELECTRODES AND DIELECTRIC LAYERS AND
METHODS OF FABRICATING THE SAME**

Date: June 24, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**AMENDMENT AND RESPONSE TO
RESTRICTION REQUIREMENT**

Sir:

Applicants provide the present Amendment to address the issues raised in the Office Action mailed May 25, 2005.

If any extension of time for the accompanying Amendment and Response is required, Applicants request that this be considered a petition therefore. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to Deposit Account No. 50-0220.

Amendments to the Title begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims, which begins on page 3 of this paper.

Remarks begin on page 6 of this paper.

In re: Chung *et al.*
Serial No.: 10/803,640
Filed: March 18, 2004
Page 2 of 6

Amendments to the Title:

Please replace the title with the following amended title:

METHODS OF FABRICATING INTEGRATED CIRCUIT DEVICES HAVING BARRIER
LAYERS BETWEEN UPPER ELECTRODES AND DIELECTRIC LAYERS AND
~~METHODS OF FABRICATING THE SAME~~